## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.						09/517,127
Filing Date						. March 2, 2000
Inventor					Scot	t E. Moore et al.
Assignee					Micron T	Technology, Inc.
Group Art Unit						3723
Examiner						T. Eley
Attorney's Docket No.						MI22-1246
Title: "Semiconductor	Processor	Systems,	Α	System	Configured	to Provide a
Semiconductor V	Vorkpiece Pi	rocess Fluid	A) b	s Amend	ed)"	•

PRELIMINARY AMENDMENT

To:

**Box RCE** 

Assistant Commissioner for Patents

Washington, D.C. 20231

From:

James D. Shaurette (Tel. 509-624-4276; Fax 509-838-3424)

Sir:

Wells, St. John, P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Please enter the following amendments prior to examining the above identified application:

## **REMARKS**

Claims 1-5, 7-28, 30-33, 39-65, 67, 130-136 and 138-144 were indicated to be allowable in the parent application. This request for continued examination is being submitted out of an overabundance of caution to have the allowed claims considered in light of additional prior art. Such art is cited in an accompanying Information Disclosure Statement.